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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sreenivasan et al.
App. No.: 10/614,716 GPAU: 1722
Filing Date: 07/07/2003 Examiner: Donald Heckenberg
Dkt. No.: P90-32V30 Conf. No.: 7603
For: CONFORMING TEMPLATE FOR PATTERNING LIQUIDS DISPOSED ON
SUBSTRATES

INFORMATION DISCLOSURE STATEMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure and pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98. The undersigned brings the patents, publications, applications or other information identified in the attached:

☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449

☐ Other:

to the Examiner's attention in the above-identified application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed:

before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

Applicants do not believe that any additional fees are due, but if the Commissioner believes additional fees are due, the

Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 502650.

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I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as either First Class Mail or Express Mail, in an envelope addressed to the Commissioner for Patents.

Signed: Katrina Prati

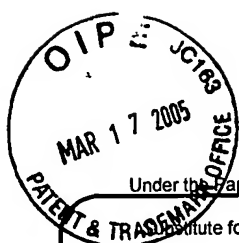
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STATEMENT BY APPLICANT**

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Sheet 1 of 4

Complete if Known

Application Number	10/614,716
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First Named Inventor	Sreenivasan et al.
Art Unit	1732
Examiner Name	Unassigned
Attorney Docket Number	P90-32V30

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	C1	US- 2003/0113638-A1	6/19/2003	Mancini et al.	
	C2	US- 6,716,754-B2	4/6/2004	Hofmann et al.	
	C3	US- 6,517,977-B2	2/11/2003	Resnick et al.	
	C4	US- 2002/0135099-A1	9/26/2002	Robinson et al.	
	C5	US- 5,849,222	12/15/1998	Jen et al.	
	C6	US- 6,391,217-B2	5/21/2002	Schaffer et al.	
	C7	US- 6,696,220-B2	2/24/2004	Bailey et al.	
	C8	US- 2002/0177319-A1	11/28/2002	Chou	
	C9	US- 6,518,189-B1	2/11/2003	Chou	
	C10	US- 6,713,238-B1	3/30/2004	Chou et al.	
	C11	US- 6,646,662-B1	11/11/2003	Nebashi et al.	
	C12	US- 6,776,094-B1	8/17/2004	Whitesides et al.	
	C13	US- 2002/0132482-A1	9/19/2002	Chou	
	C14	US- 2002/0167117-A1	11/14/2002	Chou	
	C15	US- 6,482,742-B1	11/19/2002	Chou	
	C16	US- 2003/0080471-A1	5/1/2003	Chou	
	C17	US- 2004/0036201-A1	2/26/2004	Chou et al.	
	C18	US- 6,828,244	12/7/2004	Chou	
	C19	US- 6,809,356	10/26/2004	Chou	

FOREIGN PATENT DOCUMENTS

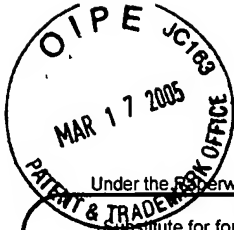
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
	C20	WO 2004/016406	2/26/2004	Sreenivasan		
	C21	WO 00/21689	4/20/2000	Chou et al.		
	C22	JP 02-92603	4/3/1990	Kurikawa et al.		
	C23	JP 02-24848	1/26/1990	Kamio		
	C24	WO 01/47003	6/28/2001	Steiner et al.		
	C25	WO 02/07199	1/24/2002	Chou		

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		Number-Kind Code ² (if known)			
	C26	US- 2004/0197843-A1	10/7/2004	Chou et al.	
	C27	US- 2004/0156108-A1	8/12/2004	Chou et al.	
	C28	US- 2004/0137734-A1	7/15/2004	Chou et al.	
	C29	US- 2004/0131718-A1	7/8/2004	Chou et al.	
	C30	US- 2004/0118809-A1	6/24/2004	Chou et al.	
	C31	US- 2004/0046288-A1	3/11/2004	Chou	
	C32	US- 2003/0080472-A1	5/1/2003	Chou	
	C33	US- 2003/0034329-A1	2/20/2003	Chou	
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	C39	US- 5,028,366	7/2/1991	Harakal et al.	
	C40	US- 5,601,641	2/11/1997	Stephens	
	C41	US- 5,849,209	12/15/1998	Kindt-Larsen et al.	
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	C42	WO 2004/114016	12/29/2004	Chou et al.		
	C43	WO 99/05724	2/4/1999	Chou et al.		
	C44	WO 03/010289	2/6/2003	Chou et al.		
	C45	WO 03/079416	9/25/2003	Chou		
	C46	WO 03/099536	12/4/2003	Chou et al.		
	C47	WO 2004/013693	2/12/2004	Watts et al.		

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Application Number 10/614,716

Filing Date 07/07/2003

First Named Inventor Sreenivasan et al.

Art Unit 1732

Examiner Name Unassigned

Attorney Docket Number P90-32V30

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of

4

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	C48	Heidari, "Nanoimprint Lithography at the 6 in. Wafer Scale," J. Vac. Sci. Technol. B 18(6), pp. 3557 - 3560, 11/1/2000.	
	C49	Translation of Japanese Patent 02-92603	
	C50	Translation of Japanese Patent 02-24848	
	C51	Chou et al., "Ultrafast and Direct Imprint of Nanostructures in Silicon," Nature, Col. 417, (June 2002), pp. 835-837, 6/1/2002.	
	C52	Chou et al., "Nanoimprint Lithography," Journal of Vacuum Science Technology B 14(16), pp. 4129-4133, 11/1/1996.	
	C53	Colburn et al., "Development and Advantages of Step-and-Flash Lithography," Solid State Technology, 7/1/2001.	
	C54	Colburn et al., "Characterization and Modeling of Volumetric and Mechanical Properties for Step and Flash Imprint Lithography Photopolymers," Journal of Vacuum Science Technology. Vol b. 19(6), 11/1/2001.	
	C55	Bailey et al., "Step and Flash Imprint Lithography: Defect Analysis," Journal of Vacuum Science, B 19(6), pp. 2806-2810, 11/1/2001.	
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	C57	Schneider et al., "Stripes of Partially Fluorinated Alkyl Chains: Dipolar Langmuir Monolayers."	

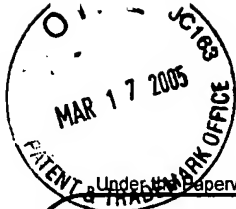
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	C58	Resnick et al., "High Resolution Templates for Step and Flash Imprint Lithography," Journal of Microlithography, Microfabrication, and Microsystems. Vol 1. No. 3., 10/1/2002	
	C59	Chou, "Nanoimprint Lithography and Lithographically Induced Self-Assembly," MRS Bulletin, pp. 512 - 517, 7/1/2001.	
	C60	Colburn et al., "Step and Flash Imprint Lithography for sub-100 nm Patterning," Proceedings of SPIE Vol. 3997, pp. 453 - 457, 1/1/2000.	

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